

FIGURE 1

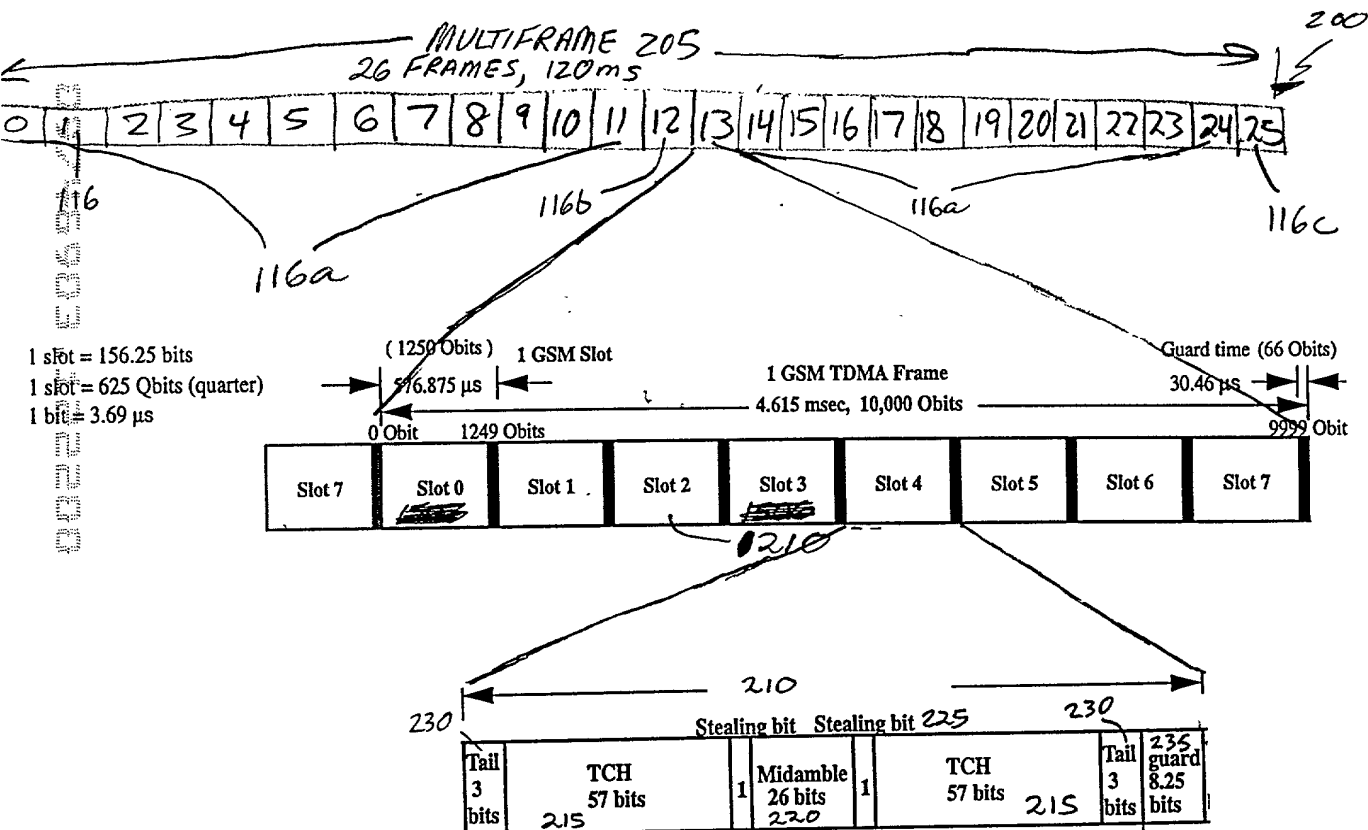


FIGURE 2A

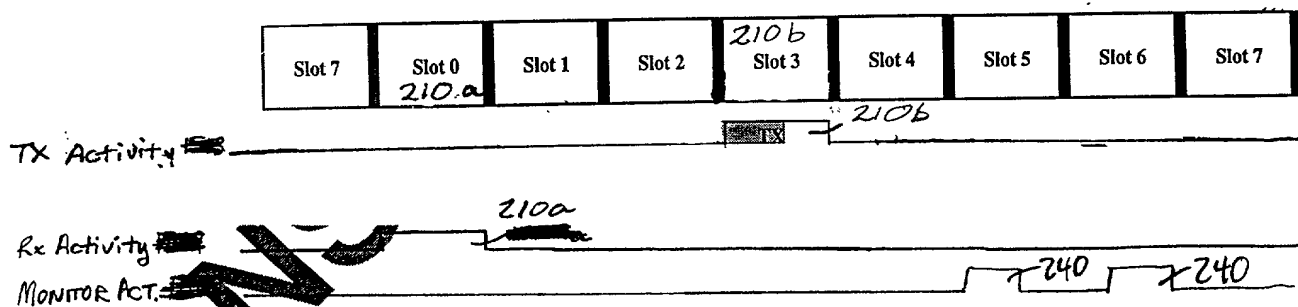
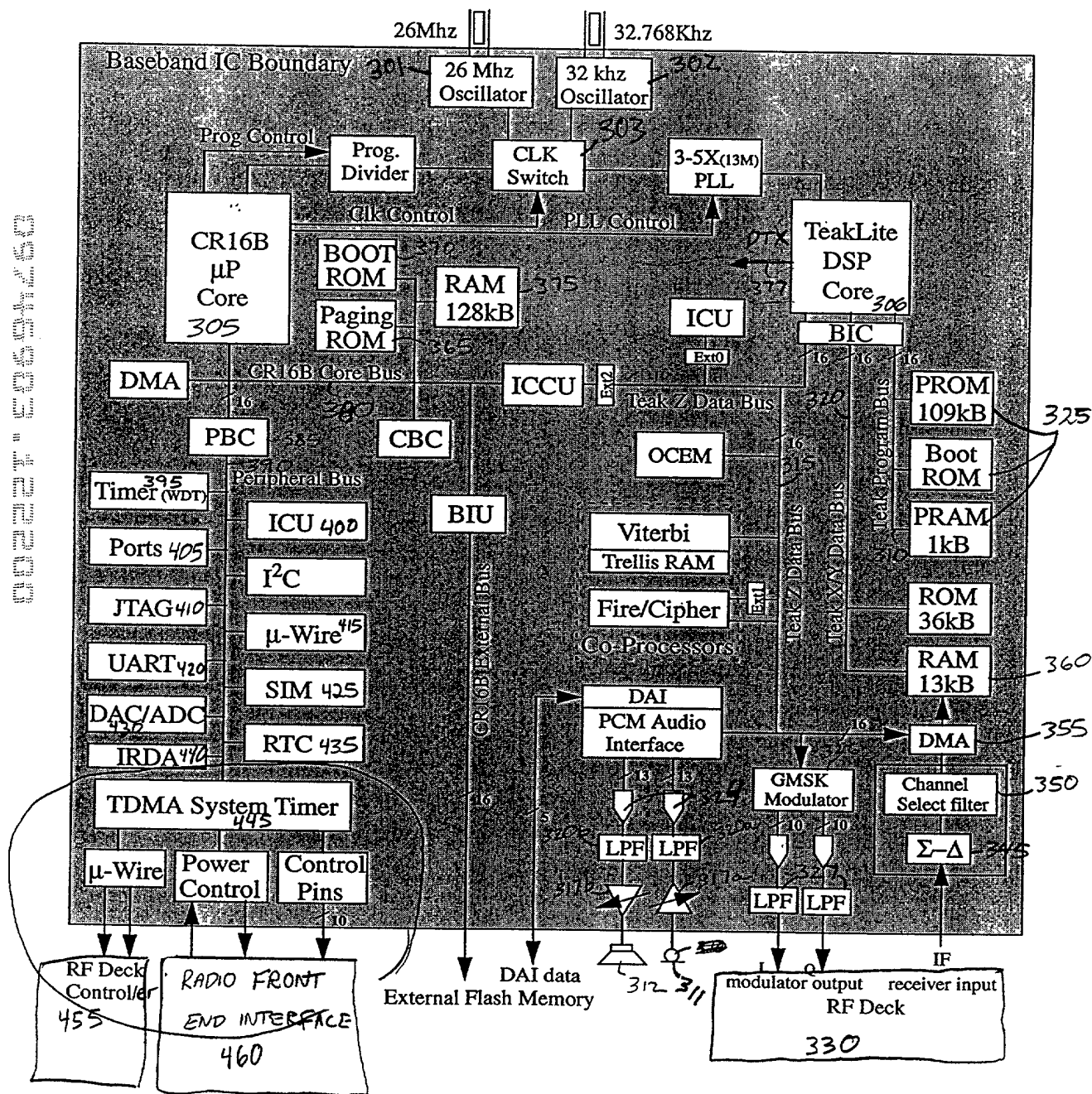


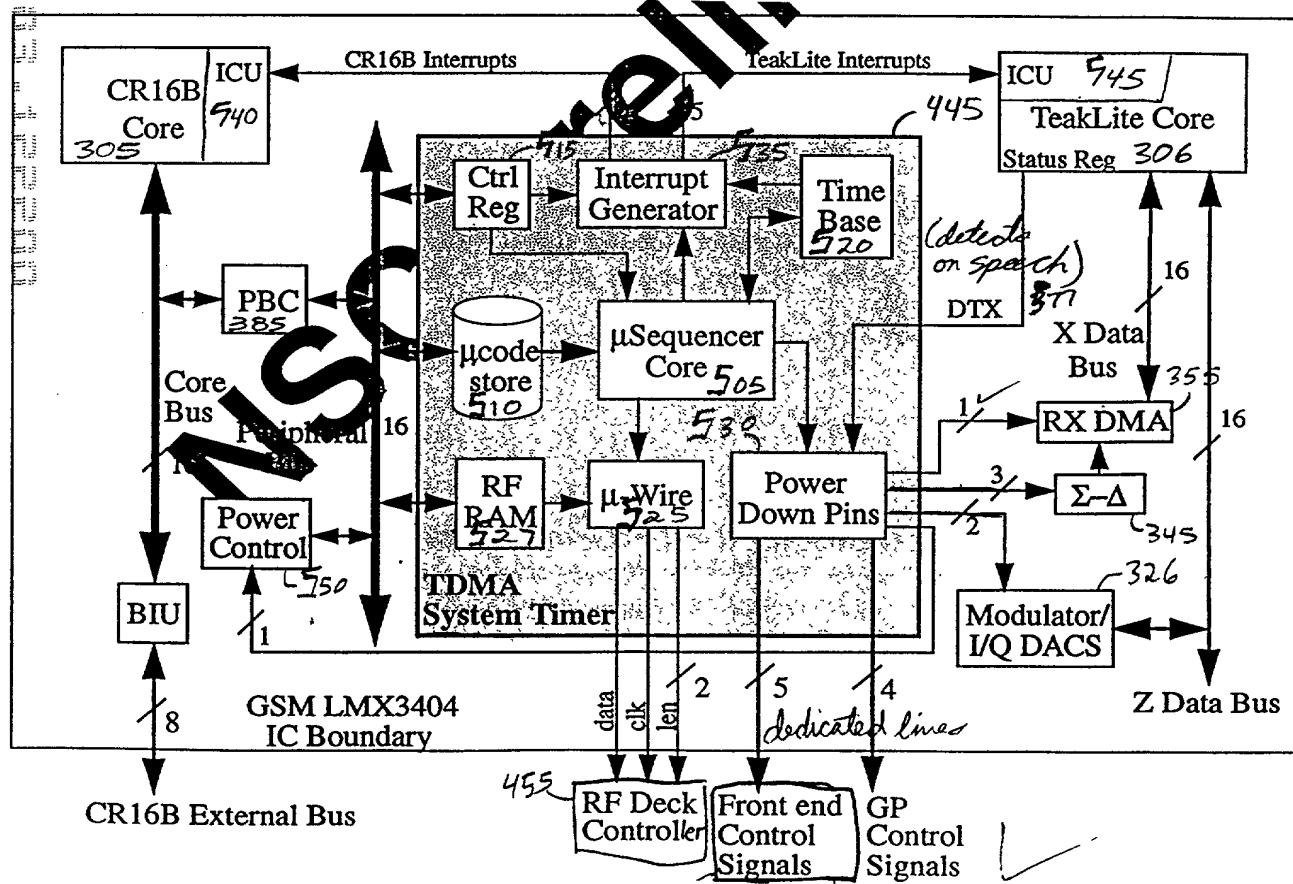
FIGURE 2B

```

graph TD
    Timer[TDMA System 445 Timer]
    subgraph System445 [ ]
        direction LR
        UWire[μ-Wire]
        Power[Power Control]
        Control[Control Pins]
    end
    RFDeck[RF Deck Controller 455]
    Radio[RADIO FRONT END INTERFACE 460]

    UWire --> RFDeck
    Power --> Radio
    Control --> Radio
    Control -- 10 --> Timer
    style System445 fill:#ccc,stroke:#333,stroke-width:1px
  
```





FIGURE

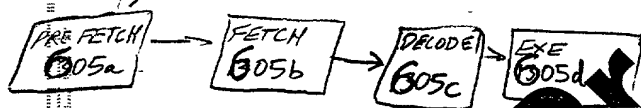
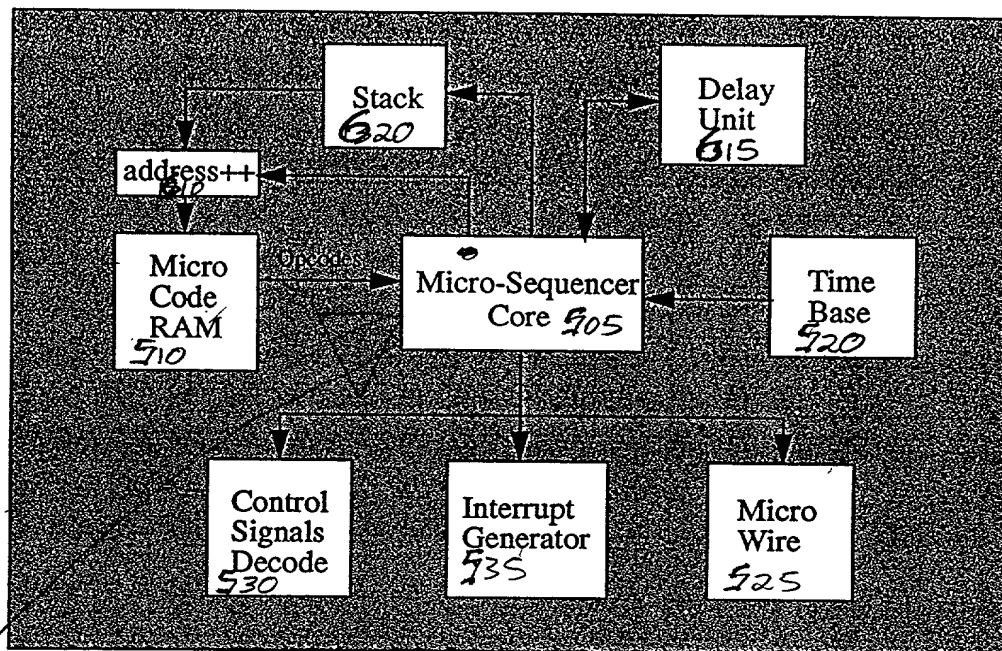


FIG. 45

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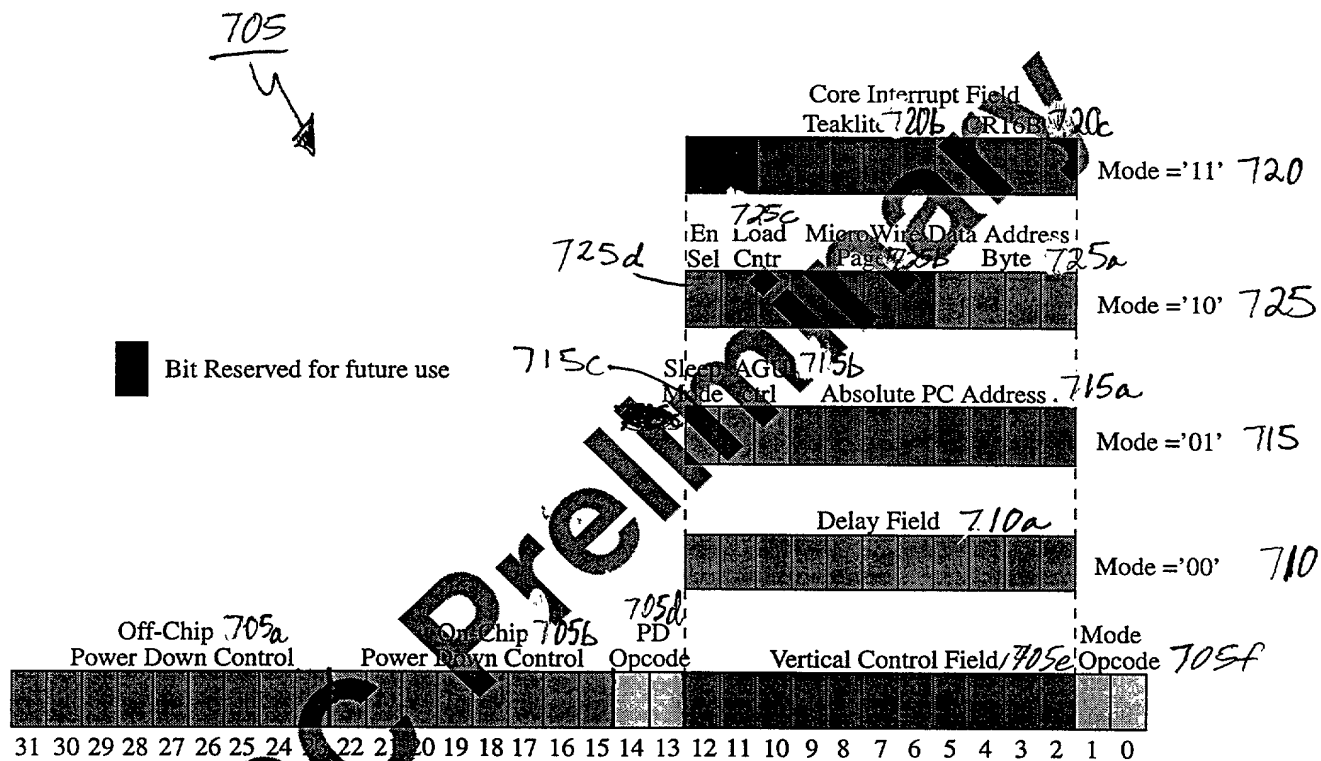


FIGURE 6

is
?

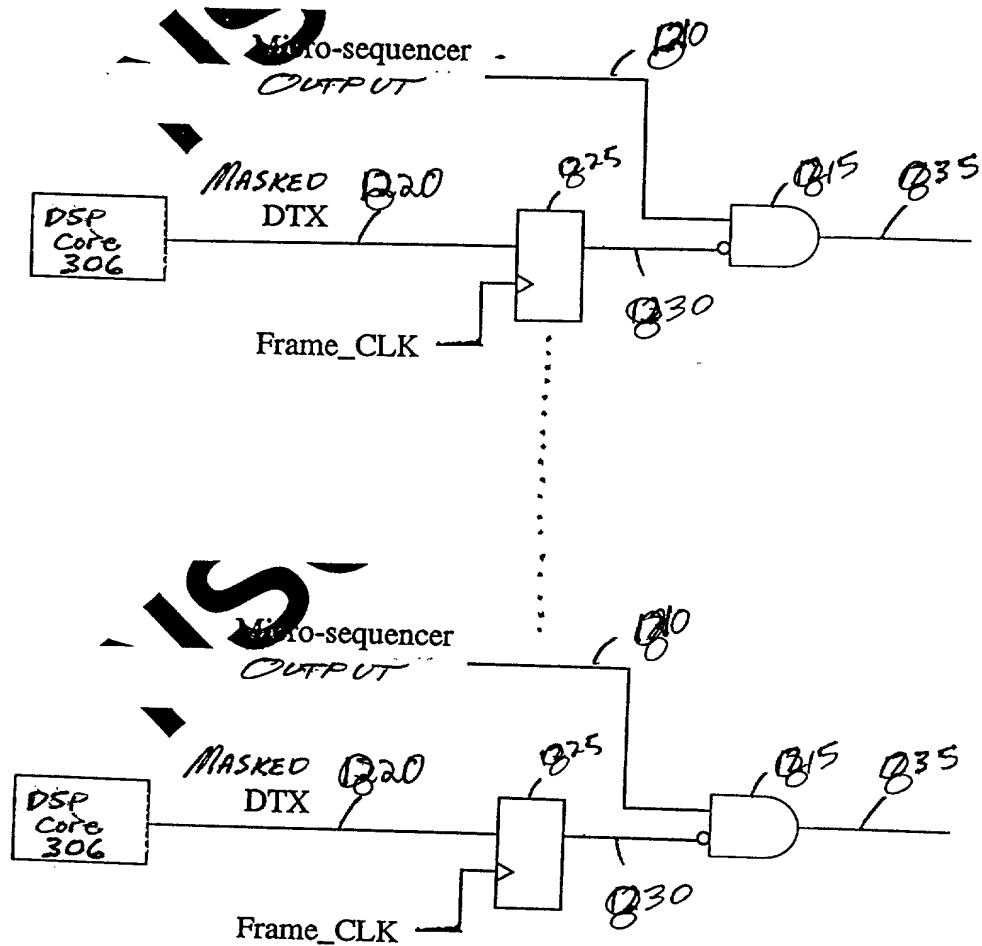


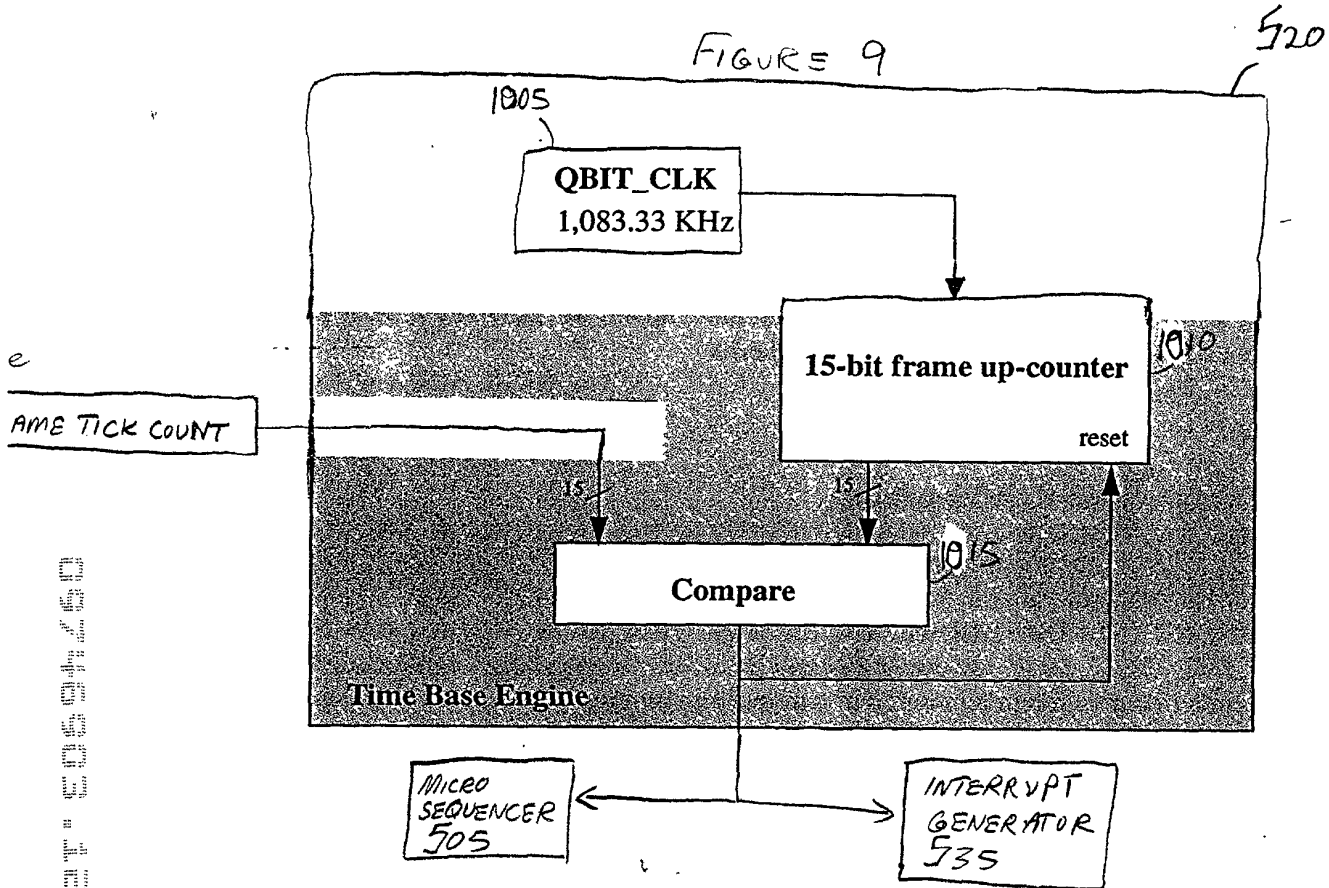
FIGURE ~~12B~~ 7

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graph TD
    963[START OF FRAME INTERRUPT] --> 966[MICROPROCESSOR PROGRAMS SYSTEM  
TIMER COPROCESSOR  
FOR UPCOMING FRAME]
    966 --> 968[SYSTEM TIMER COPROCESSOR RESETS]
    968 --> 969[SYSTEM TIMER COPROCESSOR  
BEGINS EXECUTING INSTRUCTIONS  
FOR UPCOMING FRAME]
    969 --> 984[WAIT FOR END OF FRAME]
    984 --> 987{OFFSET FROM  
END OF FRAME  
?}
    987 -- NO --> 984
  
```

FIGURE 8

FIGURE 9



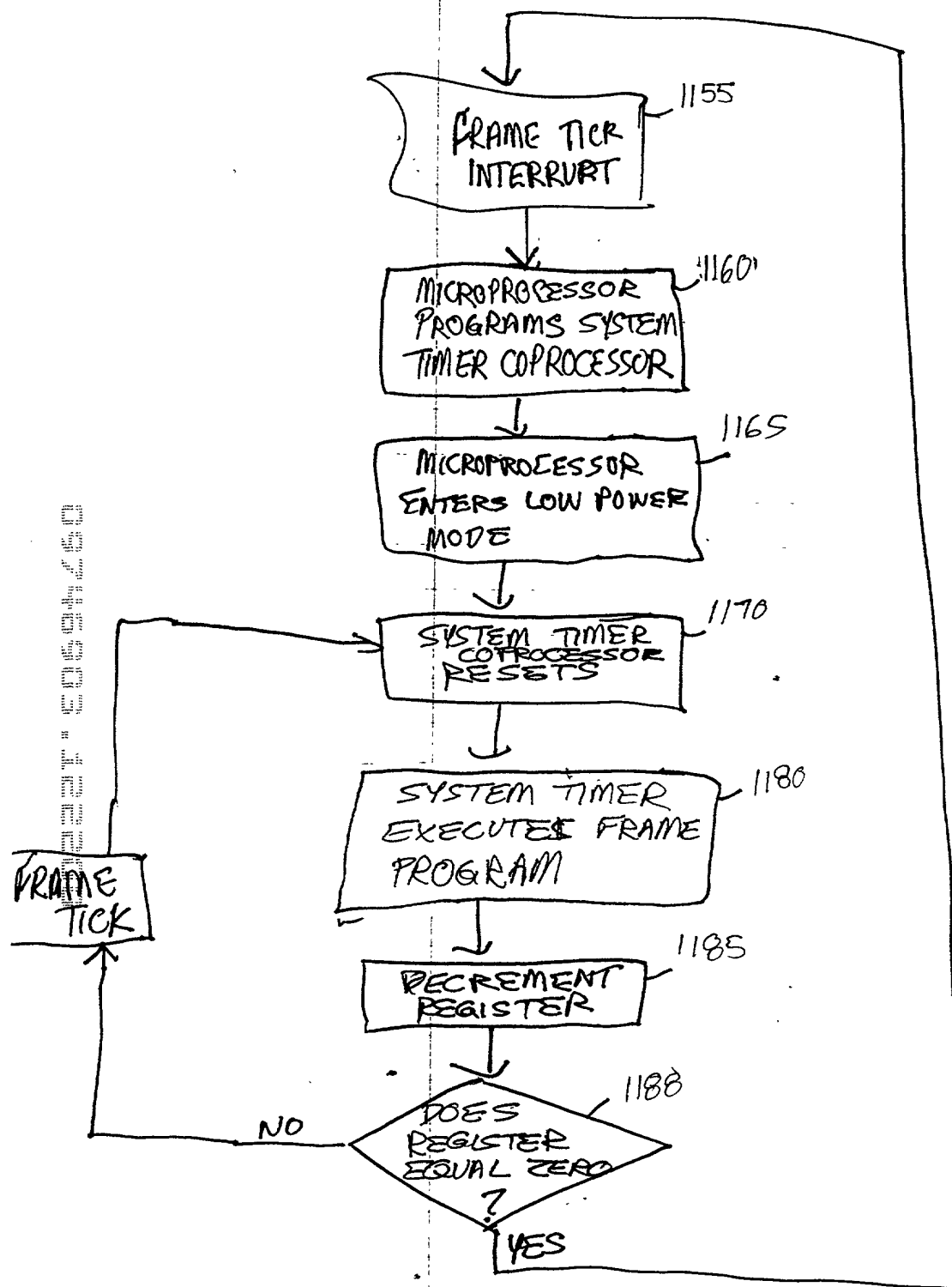


FIGURE ~~10~~ 10

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graph TD
    1205[FRAME TICK INTERRUPT] --> 1210[MICROPROCESSOR PROGRAMS SYSTEM  
TIMER COPROCESSOR]
    1210 --> 1215[SYSTEM TIMER RESETS]
    1215 --> 1220[SYSTEM TIMER CO.  
EXECUTES FRAME PROGRAM]
    1220 --> 1225[SYSTEM TIMER COPROCESSOR ENTERS  
LOW POWER SLEEP MODE]
    1225 --> 1230[SYSTEM CLOCK MAINTAINS TIMING]
    1230 --> 1235{PREDETERMINED  
OFFSET FROM  
START OF FRAME  
?}
    1235 -- NO --> 1205
    1235 -- YES --> 1230

```

FIG. 11

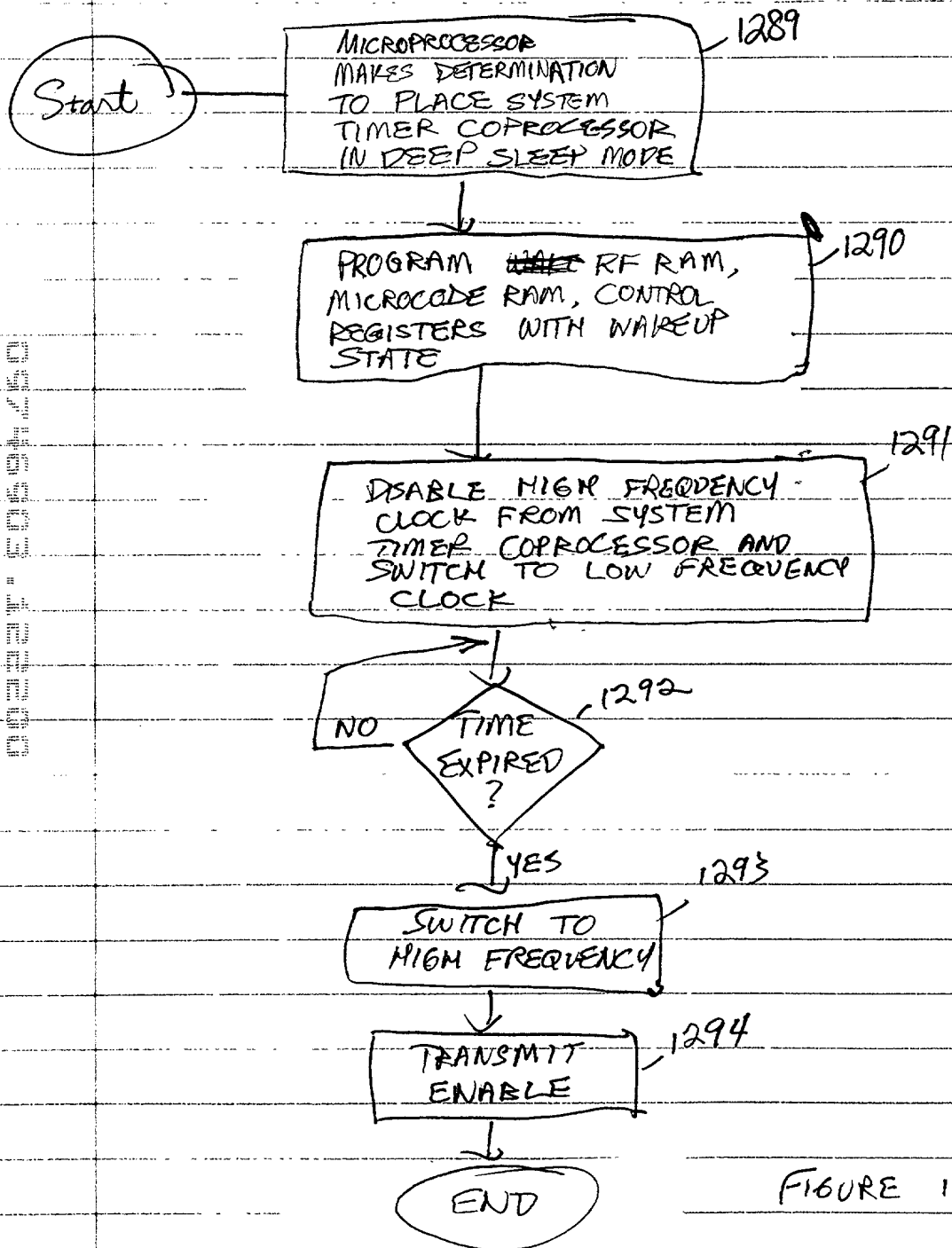


FIGURE 11B

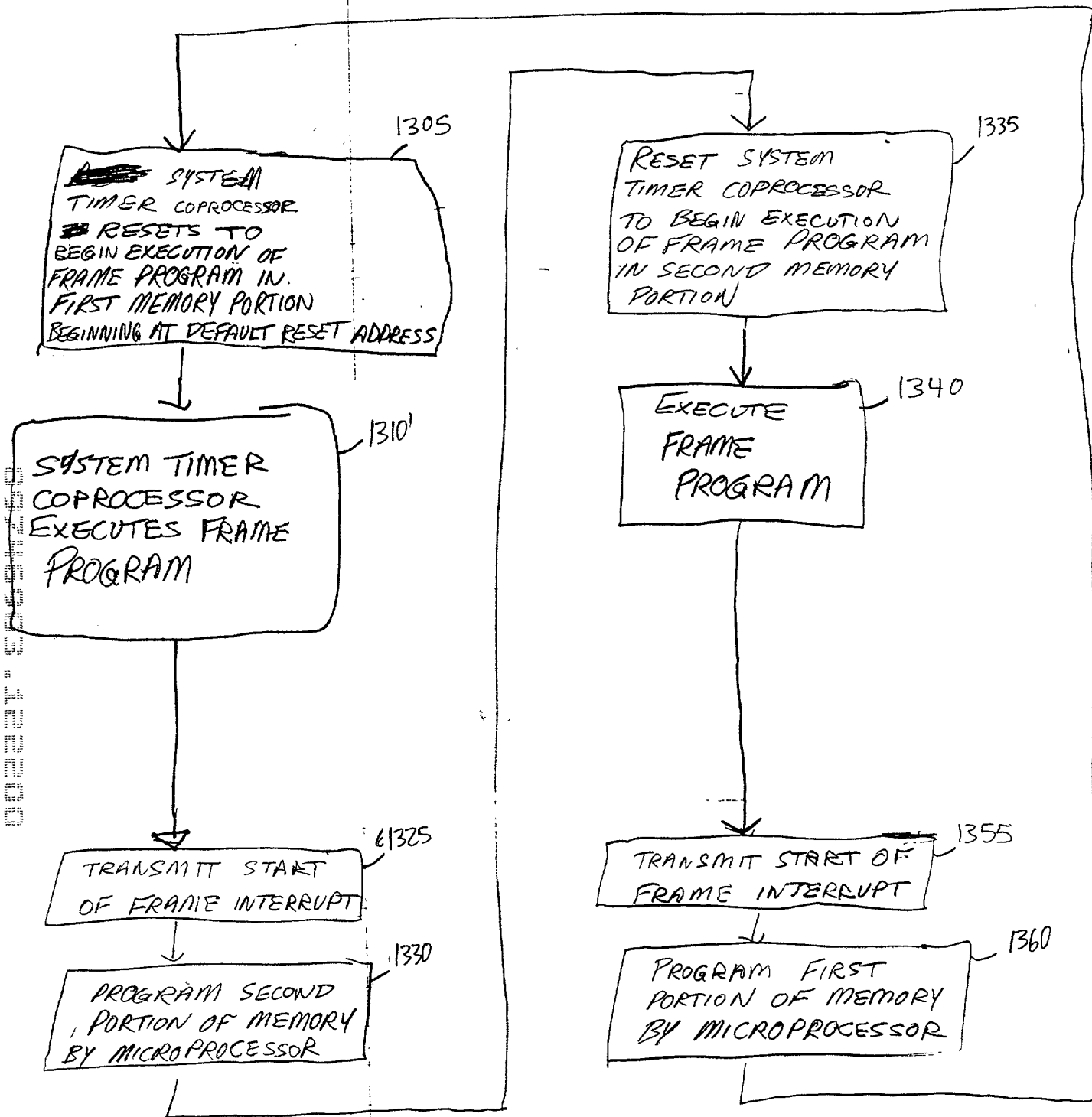


FIGURE ~~11~~ 12